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PATENT RECEIVED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE SEP 26 2001

In re Patent Application of:  
**Dabral et al.**

Application No.: **09/451,684**

Filed: **30 November 1999**

For: **Input-Output Bus Interface**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Examiner: **Chang, D.** TECHNOLOGY CENTER 2800

Art Unit: **2819**

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**RESPONSE A**

**Request for Reconsideration and Amendment under CFR §1.111**

Assistant Commissioner for Patents:

In response to the Office Action dated 14 March 2001, Applicants respectfully request reconsideration of the above-identified patent application as amended below.

**In the Claims**

Please amend claims 1, 14, and 18 to read as written below.

1. (Amended) A bus comprising:

a transmission line having a characteristic impedance;  
a pMOSFET driver to drive the transmission line, the pMOSFET driver having a source connected to a voltage source so as to be biased to a voltage V<sub>CC</sub>; and  
at least one termination device connecting the transmission line to ground to match the characteristic impedance of the transmission line.